

Senior Electrical/Systems Engineer

Are you looking for the opportunity to make a direct impact on next generation products with an industry leader? Do you have experience leading the design, development and enhancement of new and existing system technologies? Are you at ease working, collaborating and innovating in small, agile teams? Does your experience include working with embedded products that encompass hardware, system software, and application software components? Do you bring a can-do attitude and strong desire to win with you to work every day? Have you ever considered turning your car into a Transformer? If so, Data I/O could be the place for you!

Headquartered in Redmond, Washington, Data I/O is a global company with subsidiary offices in Munich, Germany and Shanghai, China and the leading provider of device programming systems and solutions for semiconductor devices. We are currently seeking a full-time senior electrical/software systems engineer to engage in all phases of new product introduction including concept, hardware and software architecture, design, prototype development and test, and manufacturing introduction. If you have experience in embedded system hardware and software design, this is the exciting opportunity you have been looking for!

Minimum Requirements:

- Experience with FPGA design, VHDL and Verilog synthesis and simulation
- Capable in Schematic and Layout design, test tools, and methodologies for both high-speed digital and analog circuits.
- Experience with embedded applications, C#, embedded Linux
- Experience with serial and high-speed protocols like USB, GigE, AXI, PCIe, SPI, JTAG, I2C etc.
- Demonstrated ability to handle multiple designs simultaneously
- Familiar with and accustomed to Agile design and development methodologies
- Ability to meet aggressive reliability, performance, cost, serviceability, and delivery targets
- Highly self-motivated team player with excellent written and verbal communication skills.
- 10+ years of experience in designing and shipping embedded hardware and software products
- Experience in designing, developing, and testing products to comply with international regulatory rules (CE, FCC, RoHS, IEC, etc.)
- Demonstrated technical leadership among teams comprised of both hardware and software engineers
- A Bachelor's degree in Electrical Engineering or a relevant field

Preferred/Desirable Qualifications:

- Experience with Mentor Graphics PADs & Power Logic software suite
- Experience with Lattice Diamond and/or Xilinx Vivado Design Suites
- Experience with Microcontrollers, Flash memory devices, FPGA's and GPIO's
- Experience with eMMC, UFS, and/or NVMe memory implementations
- Experience in the development of product/engineering specifications and requirements
- Experience working with geographically dispersed development teams

Join our team!

We offer:

- A rewarding and challenging work environment
- Opportunity to work with many of the most talented hardware and software engineers in the Puget Sound region
- PPO/HSA Health Plan, Dental & Vision, Life and Disability Plans
- Incentive Compensation
- 401k plan with company match

- Employee Stock Purchase Program and eligibility for annual RSU grants
- 4 weeks paid time off (PTO) each year
- Holiday pay and time off 12/25-1/1 each year, in addition to other standard holidays
- Redmond location with free parking and hybrid work options

Washington Pay Range: \$120,000 - \$160,000

The pay range above is the general base pay range for a successful candidate in the state listed. The successful candidate's actual pay will be based on various factors, such as work location, qualifications, and experience, so the actual starting pay may be above or below this range. At Data I/O, team members in regular, non-temporary roles are eligible for an annual bonus or sales incentive, based on their role. Most are eligible for an annual bonus based on company performance which is set at a percentage of the team member's eligible earnings in the prior year.

Data I/O Corporation is an Equal Opportunity Employer